

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	3094	(dual-band or multi-band) and (inductive circuit)	US-PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/24 14:55	
2	BRS	L2	699	1 and (integrat\$2 adj circuit)	US-PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/24 14:56	
3	BRS	L3	340	2 and capacitor	US-PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/24 14:57	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
4	BRS	L4	263	3 and series	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 14:57	
5	BRS	L5	30	4 and (impedance adj matching adj circuit)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:13	
6	BRS	L6	1240	(first adj inductance) and (second adj inductance)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:14	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L7	804	6 and capacitor	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:15	
8	BRS	L8	18	7 and (dual-band or multi-band)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:40	
9	BRS	L9	205	(impedance adj matching adj circuit) with inductor	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:41	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
10	BRS	L10	182	9 and capacitor	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:41	
11	BRS	L11	13	10 and (dual-band or multi-band)	US- PGPUB ; USPAT ; USOCR ; EPO; JPO; DERWE NT; IBM_T DB	2005/06/2 4 15:42	